

## **IN THE CLAIMS**

1. – 6. (canceled)

7. (presently amended)      An electronic appliance, comprising:

    a power supply;

    an electronic circuit board coupled with the power supply; and

    a sequencer engine coupled with the electronic circuit board, the sequencer engine to sequentially enable voltage regulator controllers on the electronic circuit board during a power up based at least in part on settings stored in non-volatile memory, the sequencer engine to retrieve delay times for a future power state change, and the sequencer engine to prevent the power supply from changing states until voltage regulator controller outputs are stable.

8. (canceled)

9. (original)      The electronic appliance of claim 7, further comprising:

    the sequencer engine to sequentially disable voltage regulator controllers on the electronic circuit board during a power down based at least in part on settings stored in non-volatile memory.

10. – 15. (canceled)

16. (presently amended)      An apparatus, comprising:

    a non-volatile memory interface;

a power supply interface;

a voltage regulator controller interface; and

control logic coupled with the non-volatile memory, power supply and voltage regulator controller interfaces, the control logic to retrieve delay times from non-volatile memory ~~and~~ to enable voltage regulator controller outputs at expiration of associated delay times in response to a power up request, and to prevent a power supply from changing states until the voltage regulator controller outputs are stable.

17. (original) The apparatus of claim 16, further comprising control logic to disable voltage regulator controller outputs at expiration of associated delay times in response to a power down request.

18. (original) The apparatus of claim 16, further comprising control logic to retrieve ramp rate settings from non-volatile memory and to set voltage regulator controller output ramp rates.

19. (original) The apparatus of claim 16, further comprising control logic to retrieve delay times for a future power state change.